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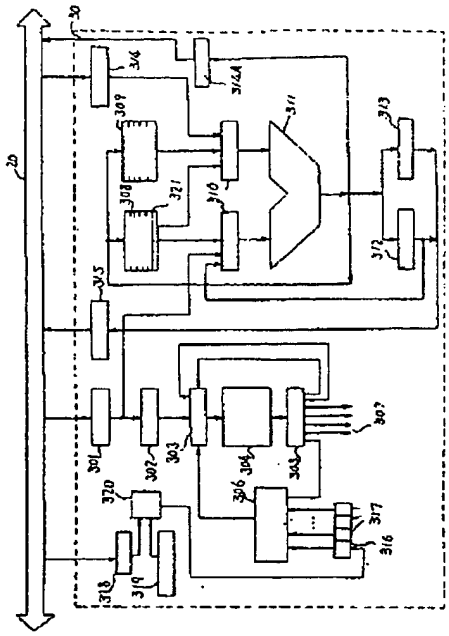
TITLE : DATA PROCESSOR



ABSTRACT : PURPOSE: To enable to restart a processing from a microinstruction at an interrupted part of an instruction, by decoding the information of a plurality of start positions of one instruction and providing a decoder corresponding to the instruction according to the result.

CONSTITUTION: An instruction is read from a main memory by taking a value of a program counter PC312 as an address and stored in an instruction register 301. An operation code in the instruction is decoded at a decoder 302 and converted into a microprogram start address on a control storage 304 and outputted to a microsequencer 303. The selection of the microinstruction is controlled with a microprogram sequencer 303 according to the result of various condition flags 317 including an interruption flag 316. When branch is made, a part of the field of the microinstruction in a pipeline register 305 is given to the microprogram sequencer 303 as the address of the next microinstruction.